

CLAIM

1. A correlation value calculating circuit comprising:
 - a 16-stage multiplier that determines a product of received data and a desreading code;
 - a 16-stage first storage that adds a result of calculation in the multiplier and data held in the 16-stage first storage and newly holds a result of addition;
 - a 16-stage first selector that selects one of data from the first storage and data obtained by inverting a polarity of said data from the first storage according to a spreading code and outputs the selected data;
 - a second storage that holds data of 256 samples;
 - a 16-stage second selector that selects one of the data held in the second storage and zero;
 - a 16-stage adder that determines a sum of the data selected and output from the first selector and the data selected and output from the second selector;
 - a third selector that selects one of results of addition in the 16-stage adder and outputs the result to the second storage; and.
 - a code generator that generates a 16-bit code that is a first basic structure of a primary synchronization code, another 16-bit code that is a first basic structure of a secondary synchronization code, and a scrambling code as desreading codes to provide to the 16-stage multiplier, and further generates a 16-bit code that is

a second basic structure of the primary synchronization code, another 16-bit code that is a second basic structure of the secondary synchronization code, and a fixed value as spreading codes to provide to the 16-stage first
5 selector.